

Biographies of NTU-MTK Wireless Faculty Members



Chong-Kuang Wang (汪重光) was born in Taiwan, 1947. He received the B.S. degree in electronic engineering from National Chao Tung University and the M.S. degree in geophysics from National Central University, Taiwan in 1970 and 1973, respectively. He received the M.S. and Ph.D. degrees in electrical engineering and computer science from University of California, Berkeley in 1979 and 1986, respectively, where he worked on MOS analog integrated circuit designs using scaled technologies. He has held industrial positions with Itron in Taiwan (1973-1977), National Semiconductor, Rockwell and IBM in California (1979-1991), where he was involved in the development of CMOS memory, data modem and disk drive VLSIs. He acted as a consultant to the Computer & Communication Research Lab of Industrial Technology Research Institute (1991-2000) and an advisor to Ministry of Education Advisory Office (1997-2001), Taiwan. From 1991 to 1998, he was with the Department of Electrical Engineering, National Central University, Taiwan, where he was a Professor. Currently, he is a professor of the Department of Electrical Engineering, National Taiwan University. His research interests are in the areas of high-speed data converters, high frequency filters, radio transceivers, and data communications.



An-Yeu (Andy) Wu (S'91-M'96) received the B.S. degree from National Taiwan University in 1987, and the M.S. and Ph.D. degrees from the University of Maryland, College Park in 1992 and 1995, respectively, all in Electrical Engineering. During 1987-1989, he served as a signal officer in the Army, Taipei, Taiwan, for his mandatory military service. During 1990-1995, he was a graduate teaching and research assistant with the Department of Electrical Engineering and Institute for Systems Research at the University of Maryland, College Park. From August 1995 to July 1996, he was a Member of Technical Staff (MTS) at AT&T Bell Laboratories, Murray Hill, NJ, working on high-speed transmission IC designs. From 1996 to July 2000, he was with the Electrical Engineering Department of National Central University, Taiwan. In August 2000, he joined the faculty of the Department of Electrical Engineering and the Graduate Institute of Electronics Engineering, National Taiwan University, where he is currently a Professor. His research interests include low-power/high-performance VLSI architectures for DSP and communication applications, adaptive/multirate signal processing, reconfigurable broadband access systems and architectures, and SoC platform for software/hardware co-design.



Tai-Cheng Lee (李泰成) was born in Taiwan, R.O.C, in 1970. He received the B.S. degree from National Taiwan University in 1992, the M.S. degree from Stanford University in 1994 and the Ph.D. degree from the University of California, Los Angeles in 2001, all in electrical engineering. He worked for LSI logic from 1994 to 1997 as a circuit design engineer. He served as an adjunct assistant professor at graduate institute of electronics engineering (GIEE), National Taiwan University from 2001 to 2002. Since 2002, he has been with electrical engineering department and GIEE, National Taiwan University, where he is an associate professor. His main research interests are in high-speed mixed-signal and analog circuit design, data converters, PLL systems and RF circuits.



Liang-Hung Lu (呂良鴻) was born in Taipei, Taiwan, in 1968. He received the B.S. and M.S. degrees in electronics engineering from National Chiao-Tung University in 1991 and 1993, respectively, and the Ph. D. degree in electrical engineering from the University of Michigan, Ann Arbor, MI, in 2001. During his graduate study, he was involved in SiGe HBT technology and monolithic microwave integrated circuit (MMIC) designs. From 2001 to 2002, he was with IBM Watson Research Center, Yorktown Heights, NY, working on low-power and RF integrated circuits for silicon-on-insulator (SOI) technology. In the August of 2002, he joined the faculty of the Graduate Institute of Electronics Engineering and the Department of Electrical Engineering, National Taiwan University, Taipei, Taiwan, where he is currently an Associate Professor. His research interests include CMOS/BiCMOS RF and mixed-signal integrated circuit designs. Dr. Lu is a member of Phi-Tau-Phi Scholastic Honor Society of Republic of China.



Jri Lee (李致毅) (M' 03) received the B.Sc. degree in electrical engineering from National Taiwan University, Taipei, Taiwan, in 1995, and the M.S. and Ph.D. degrees in electrical engineering from the University of California, Los Angeles (UCLA), both in 2003. From 1997 to 1998, he was with Academia Sinica, Taipei, Taiwan, investigating control systems for novel solid-state lasers. From 2000 to 2001, he was with Cognet Microsystems, Los Angeles, CA, and subsequently with Intel Corporation, where he worked on SONET OC-192 and OC-48 transceivers. Since 2004, he has been Assistant Professor of electrical engineering at National Taiwan University. He is currently serving on the Technical Program Committees of the International Solid-State Circuits Conference (ISSCC) and Asian Solid-State Circuits Conference (A-SSCC). His research interests include broadband data communication circuits, wireless transceivers, A/D and D/A converters, phase-locked loops and low-noise broadband amplification, and modeling of passive and active devices in deep-submicron and nanometer CMOS technologies.