



Activity Report @ ASSCC 2015

IEEE SSCS Japan Chapter

Officers of SSCS Japan Chapter

Chair: Takeshi Yamamura, Fujitsu Laboratories

Vice Chair: Makoto Ikeda, University of Tokyo

Secretary: Hiroyuki Ito, Tokyo Institute of Technology

Treasurer: Tetsuya Iizuka, University of Tokyo

Home Page: <http://www.ieee-jp.org/japancouncil/chapter/SSC-37/ssc.htm>
in Japanese

Nov. 10, 2015



Activities of SSCS Japan Chapter in 2015

I. Sponsored Seminars

1. Review Seminar of Symposium on VLSI Circuits 2015
Lecture by Professor Asad Abidi of UCLA
2. Review Seminar of ASSCC 2015 (to be held in November, 2015)
with Kansai Chapter

II. Co-sponsored Technical Meetings

1. LSI and Systems Workshop & Technical Meetings
sponsored by Integrated Circuits and Devices Committee of IEICE
(Please refer to Appendix in the last page)
2. Designers Forum & Design to Test Symposium
sponsored by VDEC of University of Tokyo

III. Sponsored Awards for Young Researchers

1. IEEE SSCS Japan Chapter Academic Research Award
for the two best student posters at LSI and Systems Workshop
2. IEEE SSCS Japan Chapter VDEC Design Award
for the best student design at VDEC Designers Forum



Seminar of 2015 Symposium on VLSI Circuits

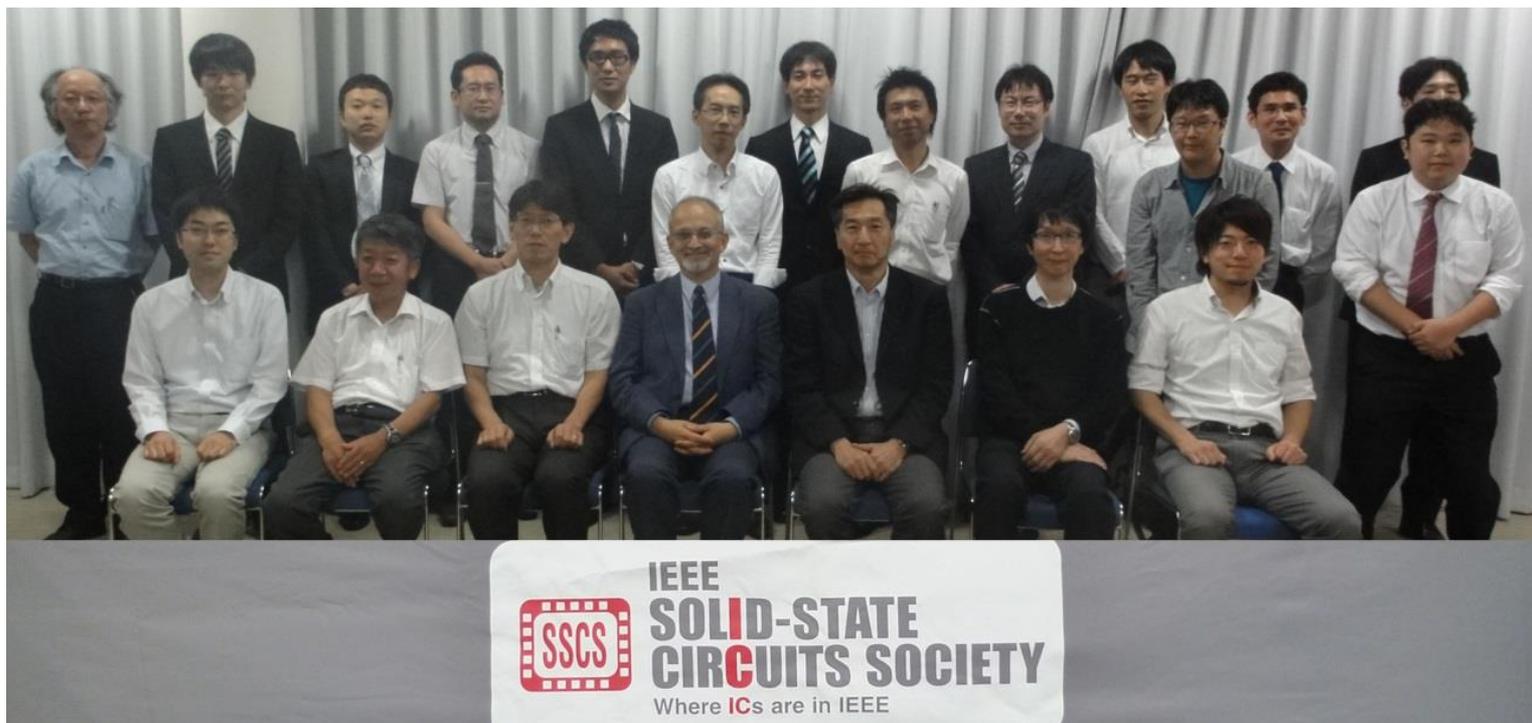
Lecture by Professor Asad Abidi

Date: June 22nd, 2015

Place: Tokyo Institute of Technology

Attendees: 76

Presentations by the 7 Japanese authors of 2015 VL



IEEE SSCS Academic Research Award

Date: May 12th, 2015

Place: Kitakyushu International Conference Hall

Two students were awarded.





Planned Activities for 2016

- Review Seminars of ISSCC 2016/ASSC2016
 - Seminar by distinguished lecturers (at least 1)
 - Co-sponsor technical meetings (at least 15)
 - Sponsor awards for young researchers
1. IEEE SSCS Japan Chapter Academic Research Awards at LSI and Systems Workshop
 2. IEEE SSCS Japan Chapter VDEC Design Award at VDEC Designers Forum



Appendix

(Technical Meetings Sponsored by ICD
Co-sponsored by IEEE SSCS Japan/Kansai Chapter)

- 2015.1.29-30 “VLSI Architecture/AI” at Kanagawa
- 2015.3.02-03 “Analog RF Circuits” at Tokyo
- 2015.3.5-6 “Micro-wave Circuits” at Fukui
- 2015.3.23-24 “ADC Lecture” at Tokyo
- 2015.04.16-17 “Memory Circuits” at Nagano
- 2015.06.25 “Analog RF Circuits” at Tokyo
- 2015.07.02-03 “Analog & Sensing Circuits” at Tokoyo
- 2015.07.19-22 “Analog RF Circuits & DC-DC Power” at Yakushima
- 2015.08.10-11 “ICDV” at Vietnam
- 2015.08.24-25 “Low Power Circuits & Silicon Devices” at Kumamoto
- 2015.10.26-27 “Automotive Circuits & Pattern Recognition” at Sendai
- 2015.11.24 “Analog RF Circuits” at Tokyo