

Dear IEEE MTT-S Members,

IEEE MTT-S Japan Chapter
Chair Hiroshi Okazaki

The 2023 IEEE MTT-S Japan/Kansai Chapter DML session will be held in Tokyo Institute of Technology (Ookayama Campus), Tokyo, Japan, on April 14. We are waiting for your attendance.

2023 IEEE MTT-S Japan/Kansai Chapter DML Session

Sponsored by: IEEE MTT-S Japan Chapter / IEEE MTT-S Kansai Chapter
Date/Time: April 14, 2023 / 16:00 - 17:00
Format: Hybrid-style
Venue: 10F Meeting Room, West Bldg. 8E, Tokyo Institute of Technology (Ookayama Campus)
Access for Ookayama Campus: <https://www.titech.ac.jp/english/0/maps>
Ookayama Campus Map: <https://www.titech.ac.jp/english/0/maps/ookayama>
Map for West Bldg.: <https://www.titech.ac.jp/english/0/maps/ookayama/ookayama>
Online: Please see Zoom information [here](#).
Admission fee: Free of charge
Lecturer: Dr. Bodhisatwa Sadhu (IBM T. J. Watson Research Center, Yorktown Heights, NY)
Title: SILICON-BASED MILLIMETER-WAVE PHASED ARRAYS FOR 5G: FUNDAMENTALS TO FUTURE TRENDS
Abstract: 5G cellular communications use millimeter-wave phased arrays to achieve high data rates and low latency. The majority of the 5G millimeter-wave infrastructure will be partially or completely based on silicon technology. This talk will discuss key aspects of silicon-based millimeter-wave phased-array module design and characterization. It will cover fundamentals of phased arrays, provide an overview of phased array antenna modules using silicon technology, and take a deep dive into an example 5G phased array antenna module. The talk will end with a peek into the future of 5G directional communications.
Please see the following URL for details (in Japanese).
<https://enotice.vtools.ieee.org/public/47751>
(Note) Program may be changed without notice.