Call for Papers

HISTory of ELectrotechnology CONference 2017

Aug 7—8, 2017, Kobe, Japan

The 5th IEEE HISTELCON 2017 will be held on August 7 and 8 in 2017 at Graduate School of Applied Informatics, University of Hyogo, Kobe, Japan. Although this HISTELCON is a flagship biennial-conference of the IEEE Region 8, this time the IEEE Japan Council History Committee will host it, cosponsored by all of nine IEEE Sections in Japan as well as technically cosponsored by the IEEE Region 8.

The campus of Graduate School of Applied Informatics, University of Hyogo, and the surrounding Kobe Port-Island Area, with their intensive high-tech activities including the RIKEN's K computer, one of the highest caliber of supercomputers in the world, will offer an excellent venue for this meeting.

It should be added that as most people may know, on January 17 in 1995 the Great Hanshin Earthquake of M 7.3 struck Kobe city and the surrounding areas, causing the catastrophic damage of more than 6,000 deaths, more than 150,000 collapsed homes, and over 300,000 people evacuated to more than 1,000 shelters. Therefore, we hope that HISTELCON 2017 will bring significant benefits to the attendees in terms of not only acquiring technical expertise but also observing miraculous post-quake recovery of Kobe.

Conference theme

–History and milestones in electro-technology innovation–

The main objective of this HISTELCON is to increase the understanding of the origins as well as the early developments in the field of electrical engineering and computing. As always, HISTELCON 2017 welcomes all papers on the history of high-tech within IEEE's areas of interest, featuring the historic expertise of any scientific and technological achievement, which has already been approved as an IEEE Milestone or may be a possible candidate for the IEEE Milestone.

The conference program will also feature high-profile keynote presentations, oral sessions, and exhibitions of IEEE Milestones attained by Japanese companies.

The conference theme is including, but not limited to;

- Origins and early developments of high-tech in electrical engineering and computing.
- The cultural/social/economical drivers for the development of high-tech within IEEE's areas of interest.
- The impact of electro-technologies on culture/society/economics.
- Governmental policies to foster high-tech of IEEE's areas in different cultures/ societies.

Abstract submission

Interested participants are invited to submit their abstracts for oral presentations to the conference committee by EDAS, https://edas.info/N23135, sending a 500-word abstract, written in English, with the title, name(s) and affiliation(s) of author(s) within 2-page in MS Word or Adobe PDF format. All abstracts will be reviewed by the Technical Program Committee. Authors of the accepted abstracts are expected to submit full-length 6-page papers that will appear in the Conference proceedings within the dedicated USB and also in IEEE Xplore. It should be noticed that for each presented paper, one presenter will have to register for the Conference.

Remarks for the submission: The Program Committee expects you to utilize your own EDAS account for the conference submission. EDAS is a conference managing service offered by EDAS Conference Service. Please check the way of getting EDAS account at the URL: https://edas.info/

More details including paper submission instruction will be announced at a later time on the conference web page: http://www.ieee-jp.org/section/kansai/histelcon2017/

Important dates
500-word abstract
■ Submission opens: Dec 1, 2016.
■ Submission deadline: Feb 14, 2017.
■ Acceptance notification: Feb 24, 2017.
Full-paper of 6-pages.
■ Submission opens: Feb 24, 2017.
■ Submission deadline: Mar 24, 2017.
Register to Participate
■ Registration opens: May 7, 2017.
■ Authors' deadline: Jun 15, 2017

(Jan 31) The deadline for abstract submission is now extended for two weeks.

Information

Event Secretariat: The Conference Secretariat, histelcon2017@ieee-jp.org.

Chairs:

- ■ General chair: Prof. Isao Shirakawa.
- ■ TPC co-chair: Prof. Kazunori Shimamura and Prof. Hironori Yamauchi.
- ■ Local committee chair: Prof. Miki Yamamoto.