



Three and half years have passed since the inauguration of our LMAG Tokyo on October 8 of 2010. Thanks to the support by Tokyo Section and LMAG members, its activities have become more and more stable and richer. In this issue, our General Assembly for 2014 and the second "Collaboration Meeting" with Student Branch, Young Professionals (YPs, former GOLD) and Women in Engineering (WIE) are reported.

### 1. Address of Prof. Kunio Tada, Chair, at the LMAG Tokyo General Assembly

Our LMAG Tokyo (Life Members Affinity Group) welcomed 49 new members on January 1 of this year. I am very pleased that the number of LMAG members thus reached 500. This means the two thirds of life members in Japan (about 760) belong to Tokyo Section and 37% of Region 10 (Asia Pacific) life members (about 1,350) are Tokyo Section Members. From the IEEE global point of view, LMAG Tokyo's position is the 12th largest in number among more than 80 LMAGs in the world, and the largest outside the USA.

With my two fellow officials I wish to enhance the activities of LMAG Tokyo which has a great number of talented members.

Your opinions, comments and suggestions are very welcome, and your active participation as well. Please send your contributions to this Newsletter as the media for exchanging opinions and finding and researching historical facts, for example.

### 2. 2014 General Assembly

The 2014 General Assembly of the IEEE Tokyo Section Life Members Affinity Group was held on March 14, from 14:00 at Kikai-Shinko-Kaikan in Tokyo. The number of attendees was 28. After the above address by Chair, Prof. Tada, the progress report on the 2013 activities was presented by Secretary, Prof. Tetsuya Miki, featuring 4 lectures jointly organized by Tokyo Section Technical Program Committee (TPC), and 3 Newsletters issued in both Japanese and English.

Next, the planning for 2014 was introduced by Vice Chair, Dr. Yukou Mochida, including a lecture organized with the TPC and a collaboration meeting with the Student Branch, the YPs and the WIE. It was also mentioned that Technical visits, Get-togethers, and exchange and collaboration with other LMAGs were under consideration. All subjects were approved.



(From left) Prof. Miki, Secretary, Prof. Tada, Chair, Dr. Mochida, Vice Chair

After the General Assembly, a lecture jointly organized with the TPC was delivered by Dr. Eiichi Ohno, recipient of "The 2013 MGA Achievement Award" and "2013 R10 Outstanding Volunteer Award", and Advisor of Mitsubishi Electric. His lecture "IEEE Milestone Activities and Contributions of Japanese Technologies to the World" dealt with the IEEE Milestone activities and its history, and also the fact that there are 18 recognitions to the Japanese contributions.

### 3. Collaboration Meeting among LMAG / Student / YPs / WIE

LMAG Tokyo, Student Branch, and Young Professionals Affinity Group under Tokyo Section, and WIE under IEEE Japan Council got together for inter-generation exchange and collaboration. Thirty-six members attended.

Date: March 15 (Sat.) 15:00 – 19:00

Venue: Katsushika-Campus of Tokyo Univ. of Science

Program:

1. Opening Address by Prof. Nobutoshi Sanada, Vice Chair of Student Activities Committee, Tokyo Section
2. Lecture "Dawn of R&D on Semiconductor Integrated Circuits – Contributions from Japan"

made a Half-Century Ago -" by Prof. Kunio Tada, LMAG Tokyo Chair

3. Comments and advice based on experiences by two panelists, Dr. Yukou Mochida, LMAG Tokyo Vice Chair, and Dr. Hisayo Momose, Member of WIE. Q&A's and discussion session.
4. Reception

Dr. Tada, Professor Emeritus of the Univ. of Tokyo, vividly introduced the R&D status on Semiconductor Integrated Circuits in Japan in the 1960s – half-century ago. He mentioned about the research on integration of bipolar logic circuits and in particular about the progress from the invention of Schottky Clamp Transistor to the prosperity of Schottky TTL. For the young audience, who live in the modern black-box ULSI era, it seemed beyond their imagination to realize the trial and error efforts by the pioneers. The early industry-academia collaborations were also introduced as one of success factors of the mission.

Answering the young audience, he introduced his own experience. He kept thinking about the unsolved problem for a long time without giving up, which led him to a sudden inspiration for invention. He said also that studying the neighboring or resembling research areas was a good way to find useful hints.

Dr. Mochida, Advisor of Freistaat Bayern Japan Office, talked about his experience of developing telecommunication systems using the benefit of progress of semiconductor industry. He also mentioned the importance and pleasure of international collaborations from his own joint works with German, Chinese or French engineers.

Dr. Momose, of Toshiba Semiconductor R&D Center, then talked about her experience in small-geometry MOSFET development in 1990s. She achieved the world record in DC and RF performance at that time, which gave her IEEE's first woman Fellow membership in Japan. She likes to think about new experiments to realize better semiconductor devices, which always keeps her excited about her profession. This statement attracted the young attendees.

In the Q&A session, Dr. Momose mentioned she expanded her professional field after finishing her study on chemistry in the university and joining the company. Prof. Tada emphasized the importance of young age, when one can make a big jump if one has passion for research and if his/her circumstances allow him/her to do research freely.

Answering the question from one of the WIE members about the job as a researcher from the view point of a woman, Dr. Momose said that she usually did not think too much about the gender difference because the job was evaluated by the

outcomes. She mentioned that it was also important not to become impatient about the disadvantage compared with men. She felt some sense of blank when she returned to her office after her one year child-care leave. However she encountered her job with fresh feelings, which resulted in her most impressive research outcome and a big success.

Further dialogues between generations were continued even more actively in the reception



(From left) Prof. Tada, Dr. Mochida and Dr. Momose



After the fruitful reception

#### 4. Lectures Celebrating a New IEEE Milestone Recognition

Sponsor: IEEE Tokyo Section

Co-sponsor: LMAG Tokyo and others

Date: May 22 (Thu.), 12:15 – 13:30

Venue: Palace Hotel Tokyo, 2<sup>nd</sup> Floor, "Tachibana"

Title: NTT's Speech Coding Technology LSP

Lecturers: Prof. Isao Shirakawa, Dr. Takehiro Moriya and Prof. Fumitada Itakura

For details please refer to the announcement sent by e-mail from Tokyo Section and LMAG Tokyo on May 1 and 2, respectively.

Please send your application by May 15 to the following address.

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