ED Japan Chapter by Mitsumasa Koyanagi, Chair

A DL talk entitled "Novel On-Chip Integrated Magnetic Micro-Inductor for RF IC" was given by Prof. Tian Ling Ren of Tsinghua University, China, at the Suzukakedai Campus of Tokyo Institute of Technology, Yokohama, Japan, on June 26, 2008. The DL abstract was as follows: An on-chip inductor was key element to RF IC. However, conventional planar spiral inductor suffered from large size and low quality factor due to energy losses. In this talk, ferrite thin-film integrated inductor was proposed. Systematic investigation of CMOS-compatible on-chip ferrite-integrated RF inductor was introduced, including inductor design and simulation, ferrite material selection, and integration process and technique. The developed novel ferrite-based inductor showed encouraging improvements for inductance and quality factor at GHz RF operation, and can be IC process compatible. This DL meeting attended by 20 people was very meaningful.

The second International Conference on 3D System Integration (3D-SIC 2008) was held at the Hitotsubashi Memorial Hall, Tokyo, JAPAN from May 12 to 13, 2008. This conference was co-sponsored by ED Japan Chapter. 3D-SIC 2008 is the only conference in Japan, which covers all the topics on 3D LSI and SiP, such as 3D process technology, materials, fabrication machines, circuits technology, design methodology, and new application. The number of attendees exceeded 200 and there were a lot of famous engineers and scientists from Japan, Korea, India, Singapore, USA, Germany, Belgium, and France. All attendees had opportunities for stimulating discussions among 3D researchers around the world.

In addition, in the first half of the year from January 2008 to June 2008, ED Japan Chapter had 3 joint technical meetings with the Japan Society of Applied Physics, the Association of Super-Advanced Electronics Technologies, etc.



Photo caption:

The DL meeting on June 26, 2008. From the fourth person from the left at the back: Prof. Tian Ling Ren, Tsinghua University, China, and Prof. Hiroshi Iwai, Partner and EDS Jr. Past President.



Photo caption:

Prof. Koyanagi, Chair of organizing committee, gave an opening remarks at 3D-SIC 2008